TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π -MOSVII)

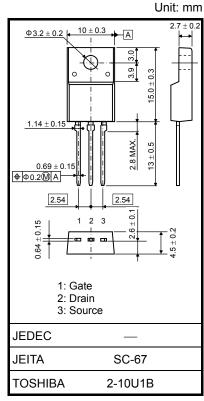
TK12A45D

Switching Regulator Applications

- Low drain-source ON-resistance: RDS (ON) = 0.43 Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 5.5 \text{ S}$ (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 450 \ V)$
- Enhancement-mode: $V_{th} = 2.0$ to 4.0 V ($V_{DS} = 10 \text{ V}$, $I_D = 1 \text{ mA}$)

| Characteristics | | Symbol | Rating | Unit |
|---|----------------|------------------|------------|------|
| Drain-source voltage | | V _{DSS} | 450 | V |
| Gate-source voltage | | V _{GSS} | ±30 | V |
| Drain current | DC (Note 1) | I _D | 12 | А |
| | Pulse (Note 1) | I _{DP} | 48 | A |
| Drain power dissipation (Tc = 25°C) | | PD | 45 | W |
| Single pulse avalanche energy (Note 2) | | E _{AS} | 292 | mJ |
| Avalanche current | | I _{AR} | 12 | А |
| Repetitive avalanche energy (Note 3) | | E _{AR} | 4.5 | mJ |
| Channel temperature | | T _{ch} | 150 | °C |
| Storage temperature range | | T _{stg} | –55 to 150 | °C |

Absolute Maximum Ratings (Ta = 25°C)



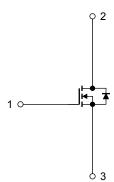
Weight : 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|--|------------------------|------|------|
| Thermal resistance, channel to case | R _{th (ch-c)} | 2.78 | °C/W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 62.5 | °C/W |

Internal Connection



Start of commercial production 2009-11

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

This transistor is an electrostatic sensitive device. Please handle with caution.

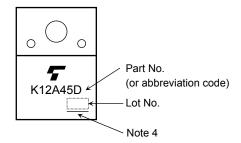
Electrical Characteristics (Ta = 25°C)

| Char | acteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-----------------------------|----------------|----------------------|---|-----|------|------|------|
| Gate leakage cu | rrent | I _{GSS} | $V_{GS}=\pm 30~V,~V_{DS}=0~V$ | _ | | ±1 | μA |
| Drain cut-off curr | rent | IDSS | $V_{DS} = 450 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$ | _ | | 10 | μA |
| Drain-source bre | akdown voltage | V (BR) DSS | $I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$ | 450 | | _ | V |
| Gate threshold v | oltage | V _{th} | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$ | 2.0 | | 4.0 | V |
| Drain-source ON | l-resistance | R _{DS (ON)} | $V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$ | _ | 0.43 | 0.52 | Ω |
| Forward transfer | admittance | Y _{fs} | $V_{DS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$ | 1.4 | 5.5 | _ | S |
| Input capacitance | e | C _{iss} | | _ | 1200 | _ | |
| Reverse transfer | capacitance | C _{rss} | $V_{DS} = 25 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$ | _ | 6 | _ | pF |
| Output capacitance | | C _{oss} | | | 120 | _ | |
| Switching time Fall time | Rise time | tr | $I_D = 6 \text{ A } V_{OUT}$ | | 25 | | |
| | Turn-on time | t _{on} | $\begin{array}{c} 0 \text{ V} \textbf{J} \textbf{L} \textbf{J} \textbf{L} \\ 50 \Omega \textbf{J} \textbf{J} \textbf{L} \\ 50 \Omega \textbf{J} \textbf{J} \textbf{J} \textbf{L} \\ V_{DD} \approx 200 \text{ V} \\ \end{array}$ $\begin{array}{c} 0 \text$ | | 60 | | - ns |
| | Fall time | t _f | | | 12 | _ | |
| | Turn-off time | t _{off} | | | 100 | _ | |
| Total gate charge | | Qg | | | 24 | | |
| Gate-source charge | | Q _{gs} | $V_{DD}\approx 360$ V, $V_{GS}=10$ V, $I_{D}=12$ A | _ | 16 | — | nC |
| Gate-drain charge | | Q _{gd} | 1 | _ | 8 | — | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|---|------------------|--|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | — | _ | _ | 12 | А |
| Pulse drain reverse current (Note 1) | I _{DRP} | — | _ | _ | 48 | А |
| Forward voltage (diode) | V _{DSF} | $I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V}$ | _ | _ | -1.7 | V |
| Reverse recovery time | t _{rr} | $I_{DR} = 12 \text{ A}, V_{GS} = 0 \text{ V},$ | _ | 1300 | _ | ns |
| Reverse recovery charge | Q _{rr} | dI _{DR} /dt = 100 A/μs | _ | 6 | _ | μC |

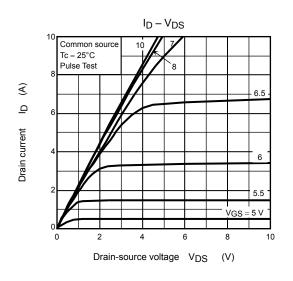
Marking

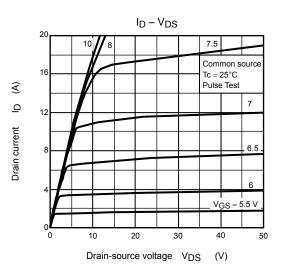


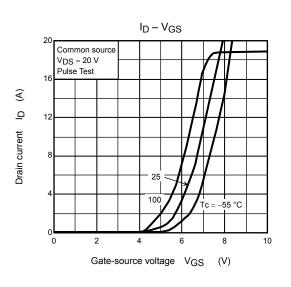
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

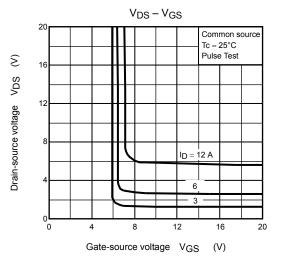
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

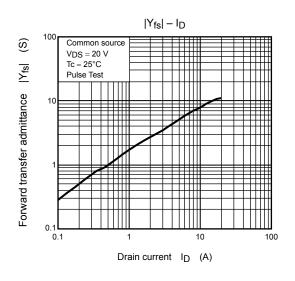
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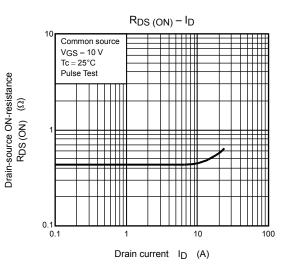




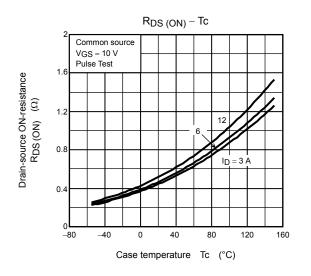


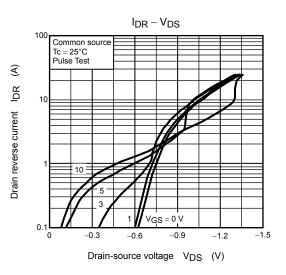


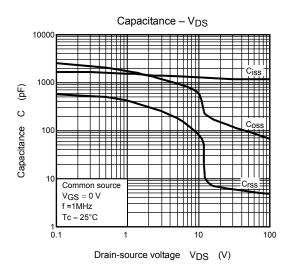




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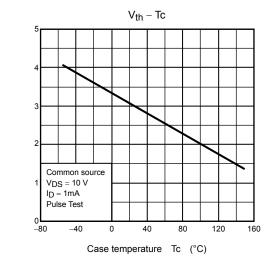
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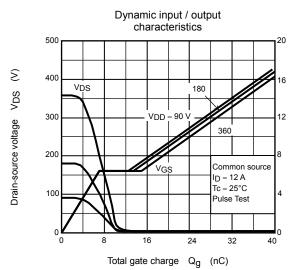
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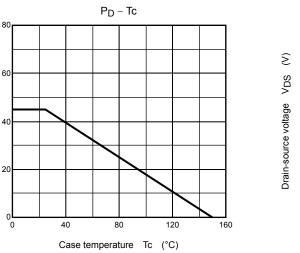
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РО

Drain power dissipation







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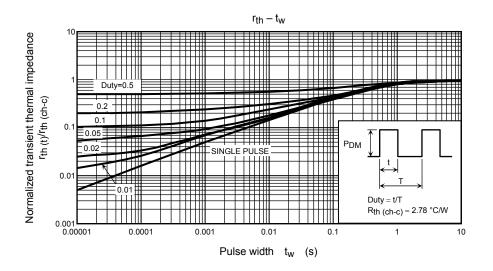
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Gate-source voltage VGS

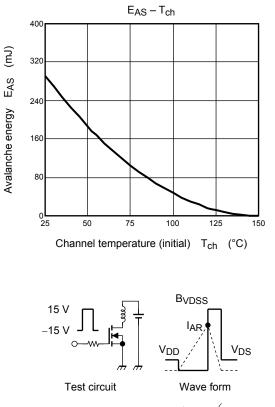
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th

Gate threshold voltage



SAFE OPERATING AREA 100 ID max (pulse) 100 μs ID max (continuous) 10 € DC operation Tc = 25°C ₽ Ħ Drain current Ш # 0.1 0.01 Single pulse Tc=25°C Curves must be derated linearly with increase in Т VDSS max temperature. 0.001 0.1 10 100 1000 Drain-source voltage V_{DS} (V)



| RG = 25 Ω | $[-1, 1]{2}$ | $\left(\frac{BVDSS}{BVDSS}-VDD}\right)$ | |
|--|---------------------|---|--|
| $V_{DD} = 90 \text{ V}, \text{ L} = 3.38 \text{ mH}$ | $LAS = \frac{1}{2}$ | (BVDSS-VDD) | |

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