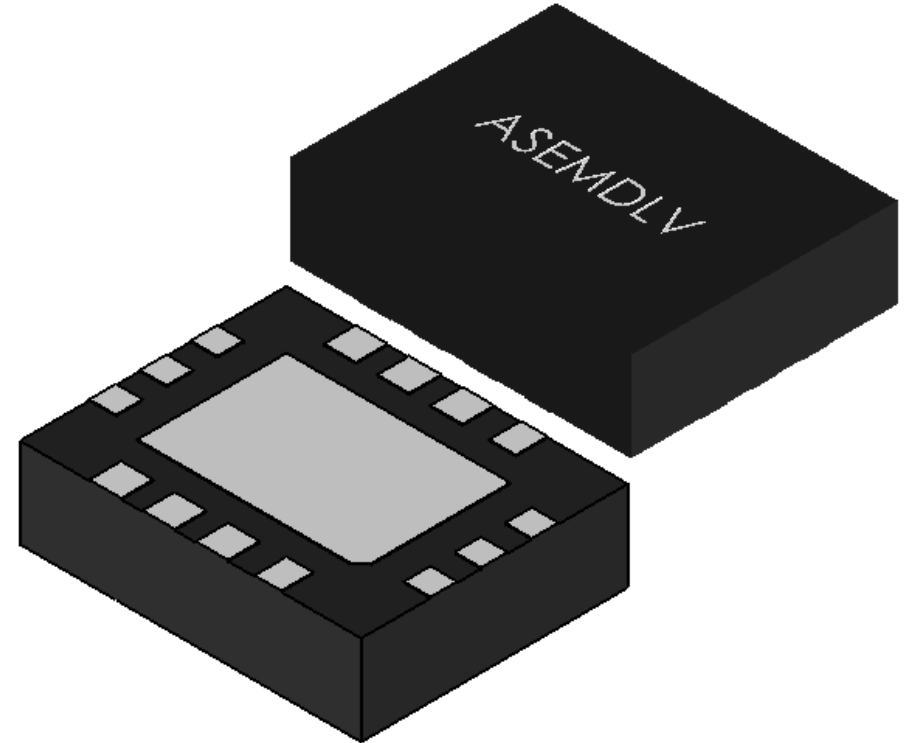
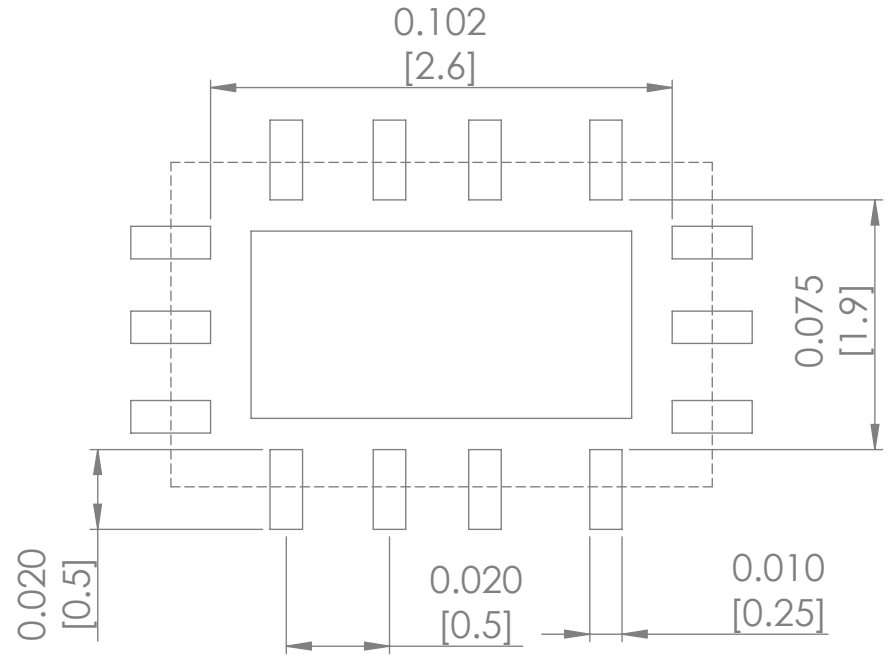



Pin	Pin Name	Pin Type	Function
1	Enable	I	Enables outputs when high and disables(tri-state) them when low
2	NC	NA	Leave unconnected or grounded
3	NC	NA	Leave unconnected or grounded
4	GND	Power	Ground
5	FS0	I	Least significant bit for frequency selection
6	FS1	I	Middle bit for frequency selection
7	FS2	I	Most significant bit for frequency selection
8	Output1+	O	Positive LVPECL Output 1
9	Output1-	O	Negative LVPECL Output 1
10	Output 2-	O	Negative LVPECL Output 2
11	Output 2+	O	Positive LVPECL Output 2
12	VDD2	Power	Power Supply 2 for LVPECL Output 2
13	VDD	Power	Power Supply
14	NC	NA	Leave unconnected or grounded

Recommended Land Pattern



UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN MM SURFACE FINISH: TOLERANCES: LINEAR: ANGULAR:				FINISH:		DEBUR AND BREAK SHARP EDGES		DO NOT SCALE DRAWING		REVISION -	
DRAWN SAAZVAT				SIGNATURE		DATE		 30332 Esperanza, Rancho Santa margarita, California 92688			
CHK'D XXXXXX								TITLE: Oscillator			
APPV'D								DWG NO. ASEMDLV			
MFG								SCALE:4:1			
Q.A						MATERIAL:		SHEET 1 OF 1			
						WEIGHT:		A3			